

CO/PC



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:  
James M. Byrd

Patent No.: 7,020,811  
Issued: March 28, 2006

Serial No.: 10/037,361  
Filed: October 29, 2001

For: SYSTEM AND METHOD FOR  
VERIFYING ERROR  
DETECTION/CORRECTION  
LOGIC

§ Atty. Docket No.: 5181-94400

§ Group Art Unit: 2138

§ I hereby certify that this correspondence is being deposited with the  
§ U.S. Postal Service as First Class Mail in an envelope addressed to:  
§ Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-  
§ 1450, on the date indicated below:

Robert C. Kowert

Signature of Representative

May 19, 2006

Date



Signature

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Certificate**

**MAY 25 2006**

**of Correction**

Dear Sir:

Transmitted herewith for filing in the captioned case are the following:

- (1) Form PTO-1050, **submitted in duplicate**. Errors which occur at important points in the captioned patent or which may otherwise affect the understanding or interpretation of the patent are thereon corrected.
- (2) A return postcard to acknowledge receipt of these materials. Please stamp and return this postcard to the undersigned.

**MAY 25 2006**

All of the errors shown in PTO-1050 are due to Patent Office oversights. A Certificate of Correction is requested under 35 U.S.C. §254. Applicants believe that no fees are required, however, should any fees be required, please deduct them from Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. Deposit Account No. 501505\5181-94400\RDR.

Respectfully submitted,



Robert C. Kowert  
Reg. No. 39,255

Meyertons, Hood, Kivlin,  
Kowert & Goetzel, P.C.  
P.O. Box 398  
Austin, Texas 78767-0398  
(512) 853-8840

Date: May 19, 2006

## CERTIFICATE OF CORRECTION

PATENT NO. : 7,020,811

DATED : March 28, 2006

INVENTOR(S) : Byrd

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 18,

Line 64, please delete "test code words comprises  $m+1$  in-bit test code words," and substitute -- test code words comprises  $m+1$  m-bit test code words—

MAILING ADDRESS OF SENDER:

Robert C. Kowert, Esq.,  
MEYERTONS, HOOD, KIVLIN, KOWERT & GOETZEL, P.C.  
P.O. Box 398  
Austin, Texas 78767-0398

PATENT NO. 7,020,811

No. of add'l. copies

UNITED STATES PATENT AND TRADEMARK OFFICE

Page 1 of 1

## CERTIFICATE OF CORRECTION

PATENT NO. : 7,020,811

DATED : March 28, 2006

INVENTOR(S) : Byrd

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 18,

Line 64, please delete "test code words comprises  $m+1$  in-bit test code words," and substitute -- test code words comprises  $m+1$  m-bit test code words—

MAILING ADDRESS OF SENDER:

Robert C. Kowert, Esq.,  
MEYERTONS, HOOD, KIVLIN, KOWERT & GOETZEL, P.C.  
P.O. Box 398  
Austin, Texas 78767-0398

PATENT NO. 7,020,811

No. of add'l. copies

**Certificate of Correction (PTO Form 1050)**

MAY 25 2006